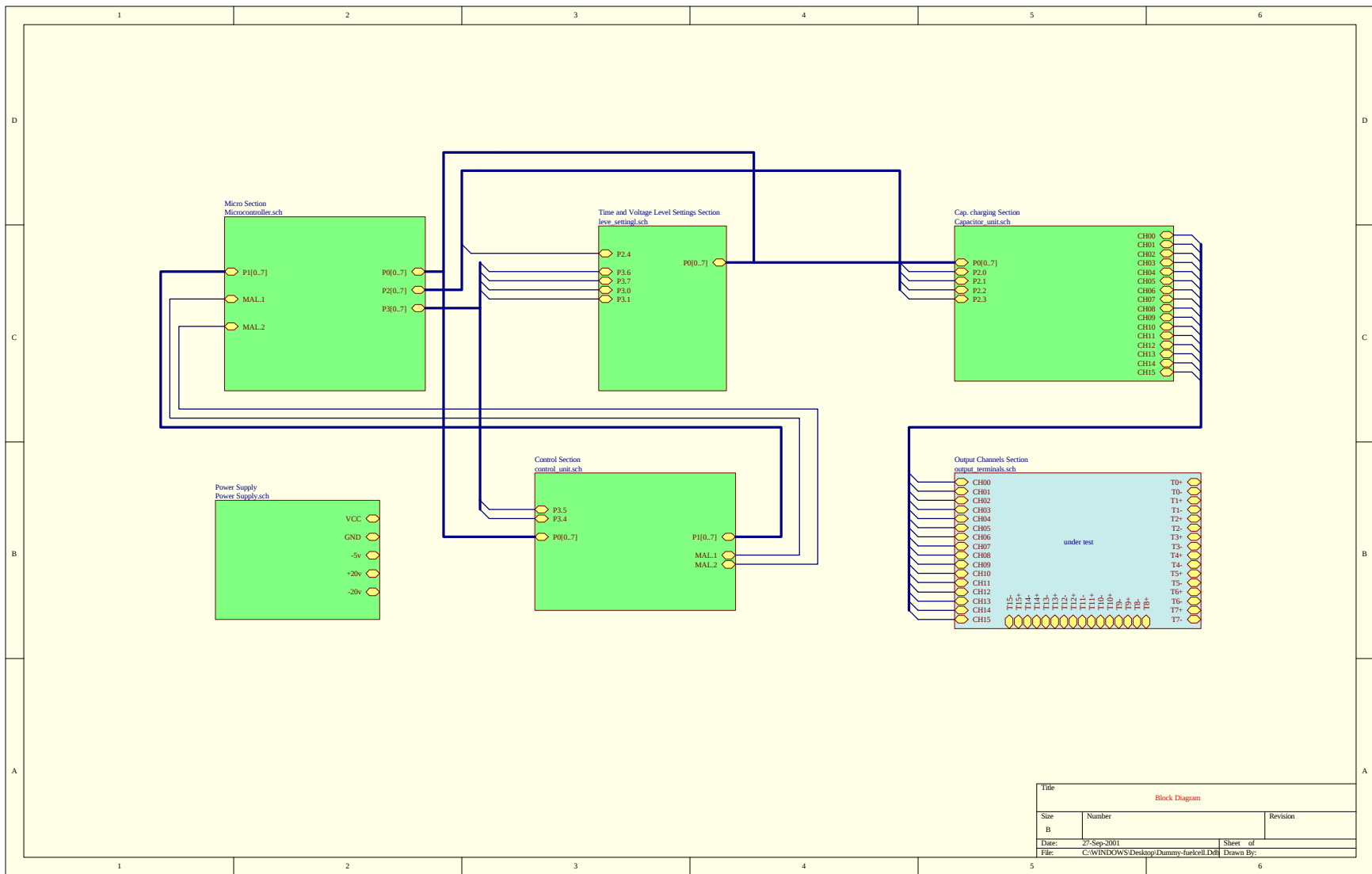
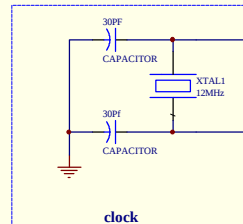
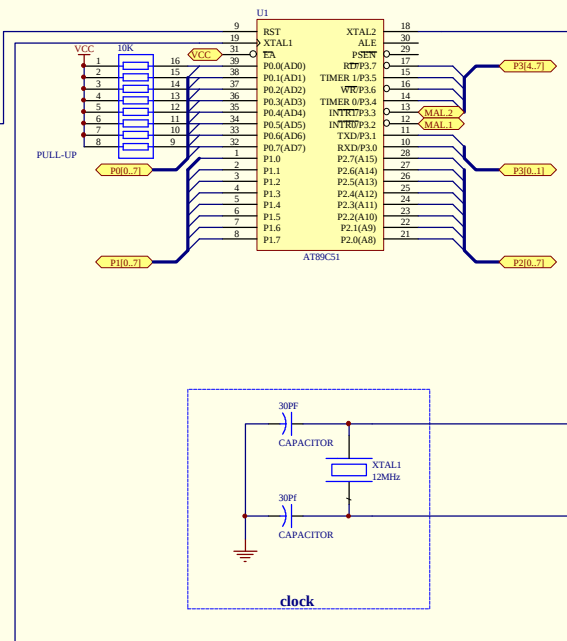
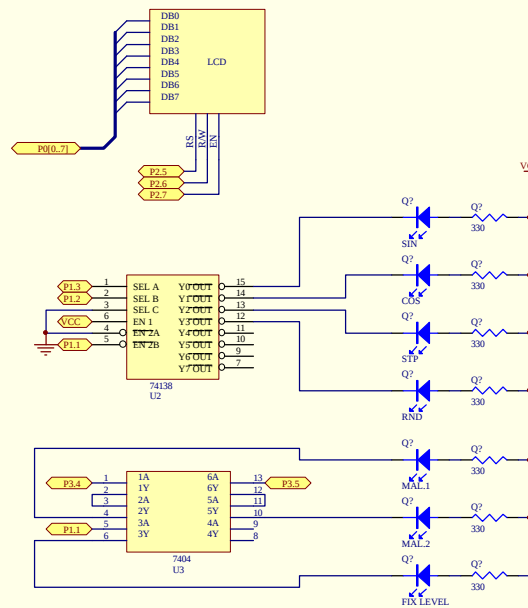
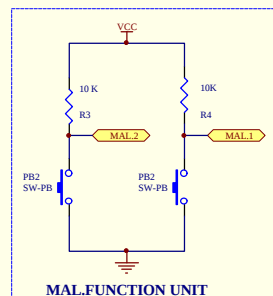
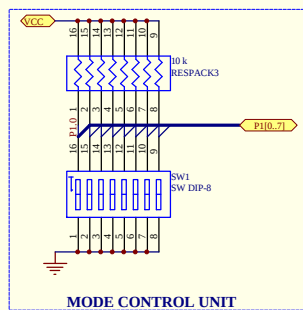


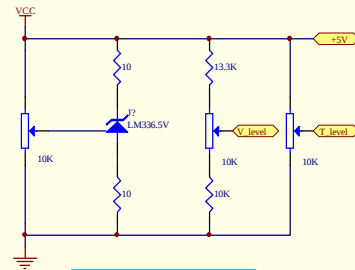




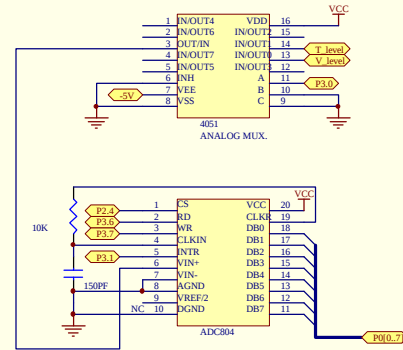
Title			Microcontroller Unit		
Size B	Number			Revision	
Date:	29-Sep-2001			Sheet of	
File:	C:\WINDOWS\Desktop\Dummy-fuacell.Dbt			Drawn By:	



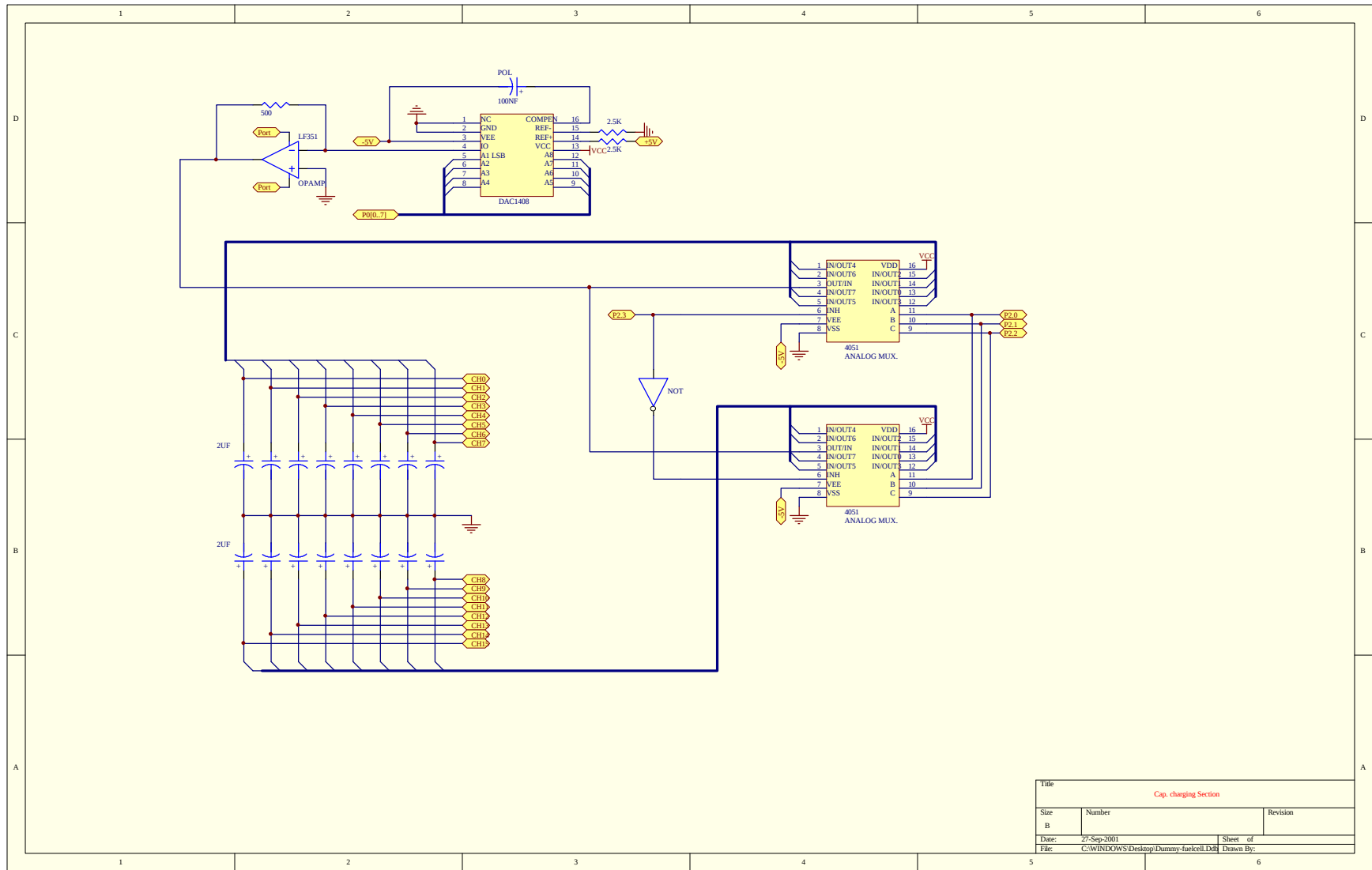
Title		
Control Unit		
Size	Number	Revision
B		
Date:	25-Sep-2001	Sheet of
File:	C:\WINDOWS\Desktop\Dummy-hurcell.Ddt	Drawn By:



Two Volumes For Settings Of Delay Time For Mat. Functions And Voltage Level

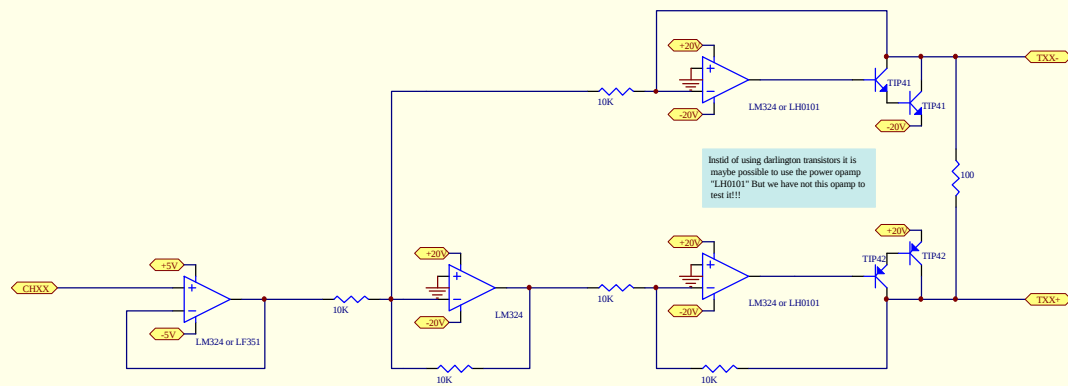


Title		
Time and Voltage Level Settings Section		
Size	Number	Revision
B		
Date:	27-Sep-2001	Sheet of
File:	C:\WINDOWS\Desktop\Dummy-hurcell.Dd	Drawn By:



Title		
Cap. changing Section		
Size	Number	Revision
B		
Date:	27-Sep-2001	Sheet of
File:	C:\WINDOWS\Desktop\Dummy-hurcell.Ddt	Drawn By:

This Circuit consists of a JFET input opamp on input channel CHXX to avoid the capacitor of discharging and an adder. The purpose Of This Part is Providing 2 output terminals that can be cable externally in series and could be able to load up to 2A current. But this part is not complete!!!
The Simulation of this circuit with Pspice has true results but in practical conditions it doesn't work properly.



Title		
Output Terminal section		
Size	Number	Revision
B		
Date:	27-Sep-2001	Sheet of
File:	C:\WINDOWS\Desktop\Dummy-huicell.Ddt	Drawn By: